

# SR26 - SR29

**PRV : 100 - 800 Volts**  
**Io : 2.0 Amperes**

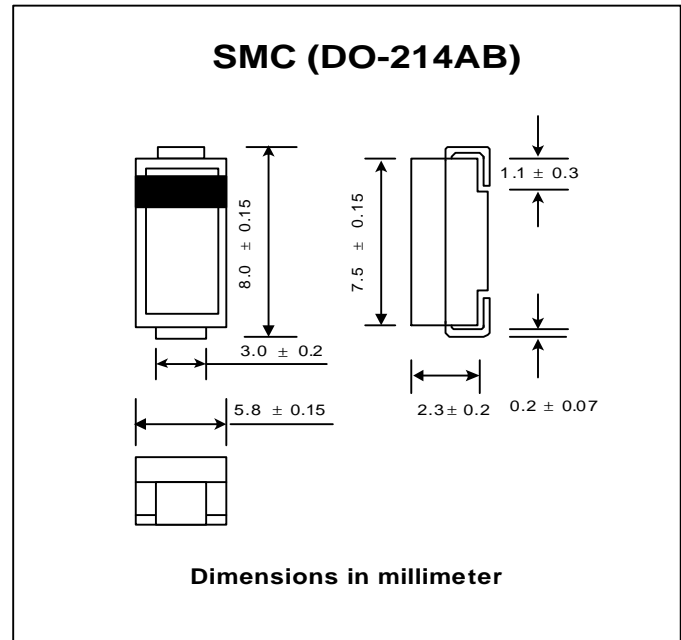
## FEATURES :

- \* High current capability
- \* High surge current capability
- \* High reliability
- \* Low reverse current
- \* Low forward voltage drop
- \* Fast switching for high efficiency
- \* Pb / RoHS Free

## MECHANICAL DATA :

- \* Case : SMC Molded plastic
- \* Epoxy : UL94V-O rate flame retardant
- \* Lead : Lead Formed for Surface Mount
- \* Polarity : Color band denotes cathode end
- \* Mounting position : Any
- \* Weight : 0.21 gram

## FAST RECOVERY SILICON SURFACE MOUNT



## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25 °C ambient temperature unless otherwise specified.  
 Single phase, half wave, 60 Hz, resistive or inductive load.  
 For capacitive load, derate current by 20%.

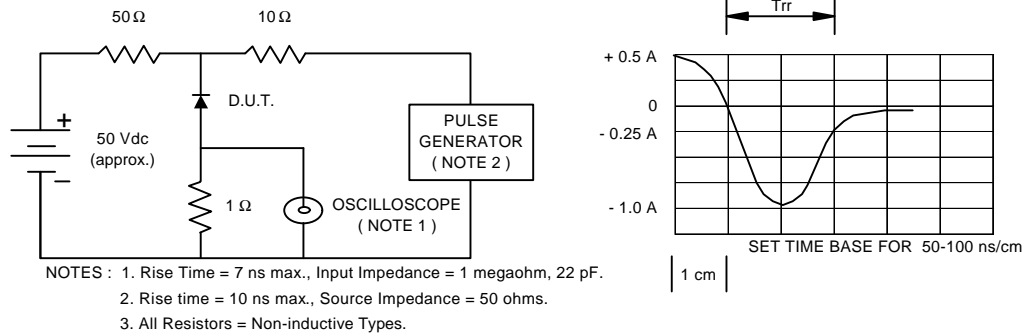
RATING	SYMBOL	SR26	SR27	SR28	SR29	UNIT
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	100	200	400	800	V
Maximum RMS Voltage	V <sub>RMS</sub>	70	140	280	560	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	100	200	400	800	V
Maximum Average Forward Current Ta = 50 °C	I <sub>F(AV)</sub>	2.0				A
Peak Forward Surge Current, 8.3ms Single half sine wave Superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	70				A
Maximum Peak Forward Voltage at I <sub>F</sub> = 2.0 A	V <sub>F</sub>	1.3				V
Maximum DC Reverse Current Ta = 25 °C at Rated DC Blocking Voltage Ta = 100 °C	I <sub>R</sub>	10				μA
	I <sub>R(H)</sub>	500				μA
Maximum Reverse Recovery Time ( Note 1 )	T <sub>rr</sub>	250				ns
Typical Junction Capacitance ( Note 2 )	C <sub>J</sub>	28				pf
Junction Temperature Range	T <sub>J</sub>	- 50 to + 125				°C
Storage Temperature Range	T <sub>STG</sub>	- 50 to + 150				°C

### Notes :

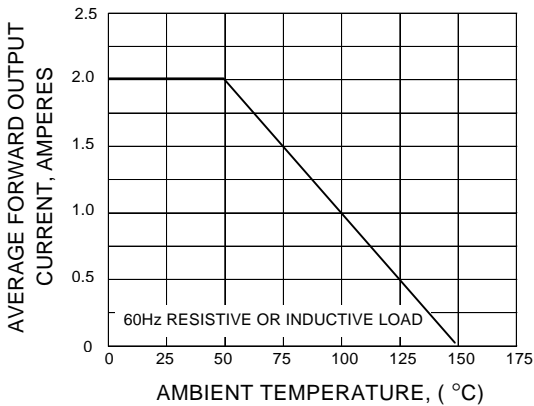
- ( 1 ) Reverse Recovery Test Conditions : I<sub>F</sub> = 0.5 A, I<sub>R</sub> = 1.0 A, I<sub>rr</sub> = 0.25 A.
- ( 2 ) Measured at 1.0 MHz and applied reverse voltage of 4.0 V<sub>DC</sub>

## RATING AND CHARACTERISTIC CURVES ( SR26 - SR29 )

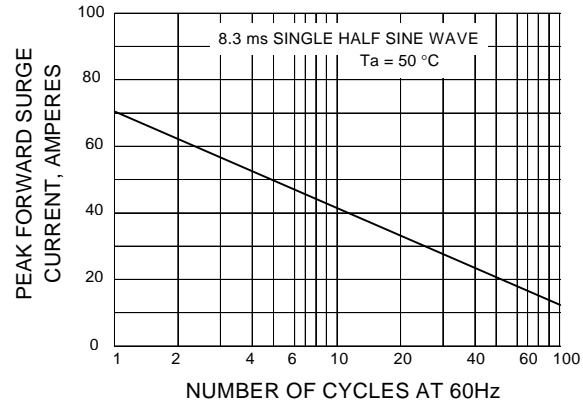
**FIG.1 - REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM**



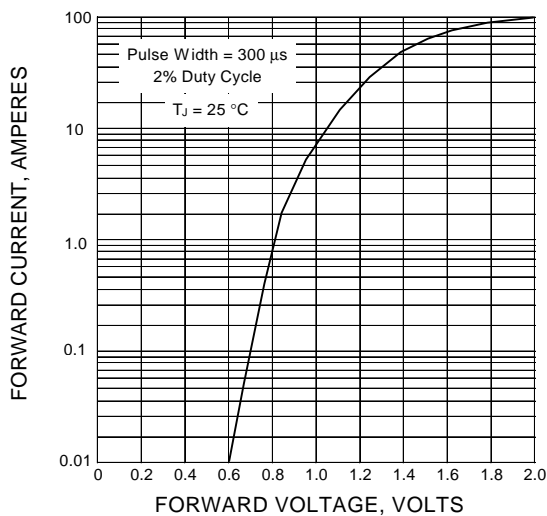
**FIG.2 - DERATING CURVE FOR OUTPUT RECTIFIED CURRENT**



**FIG.3 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT**



**FIG.4 - TYPICAL FORWARD CHARACTERISTICS**



**FIG.5 - TYPICAL REVERSE CHARACTERISTICS**

